

ABSTRACT OF THE DISCLOSURE

The present invention is broadly directed to a integrated circuit component with a scalable architecture. In one embodiment, an integrated circuit component is
5 provided comprising logic capable of being configured to interface with a first portion of a system bus, and logic capable of being configured to interface with a companion integrated circuit and to receive information that is communicated from the companion integrated circuit, which information was communicated to the companion integrated circuit via a second portion of the system bus.

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